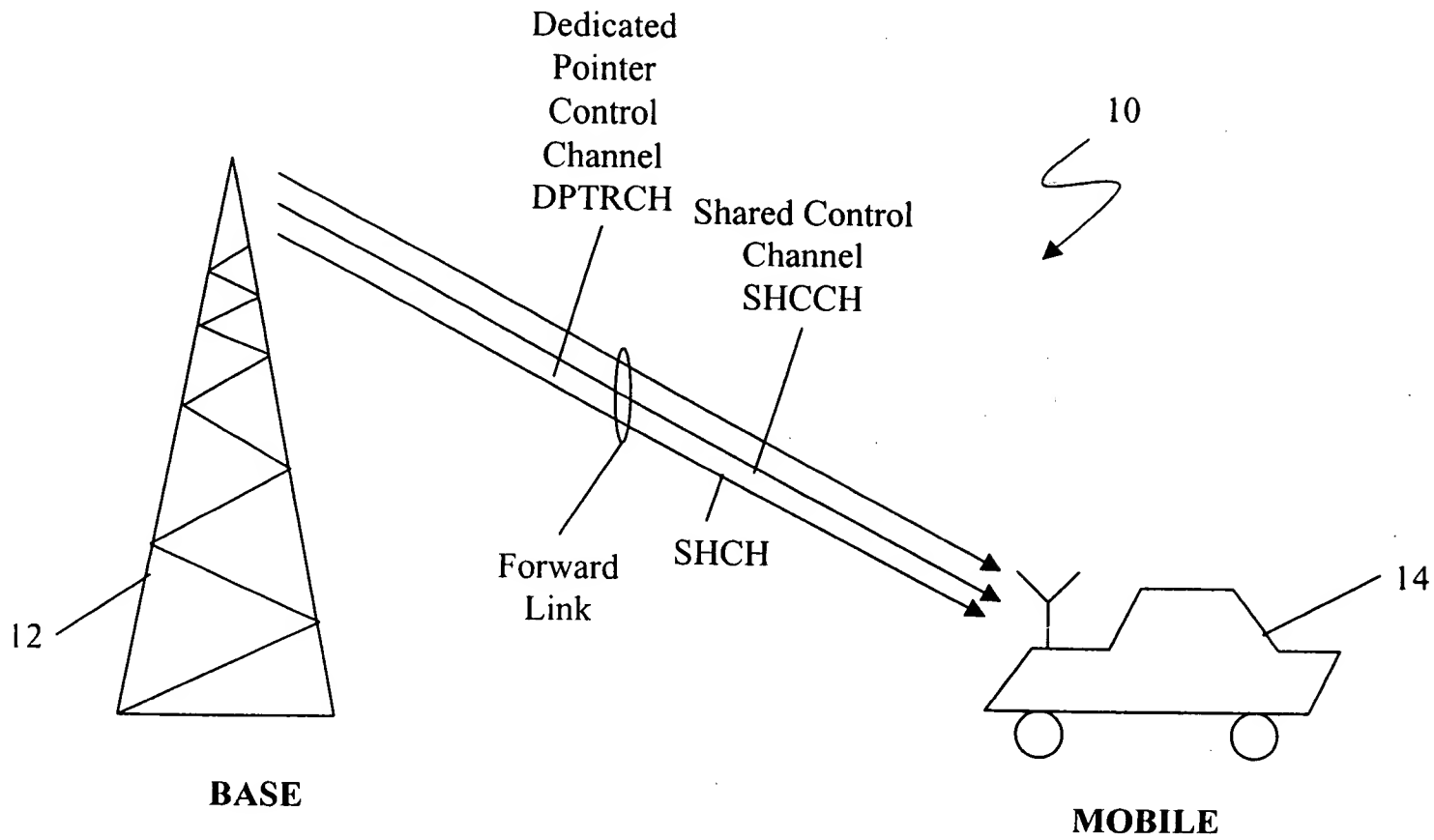
Dedicated
Pointer
Control
Channel
DPTRCH
Shared Control
Channel
SHCCH
10
Forward
Link
SHCH
12
14
BASE
MOBILE

FIG. 1

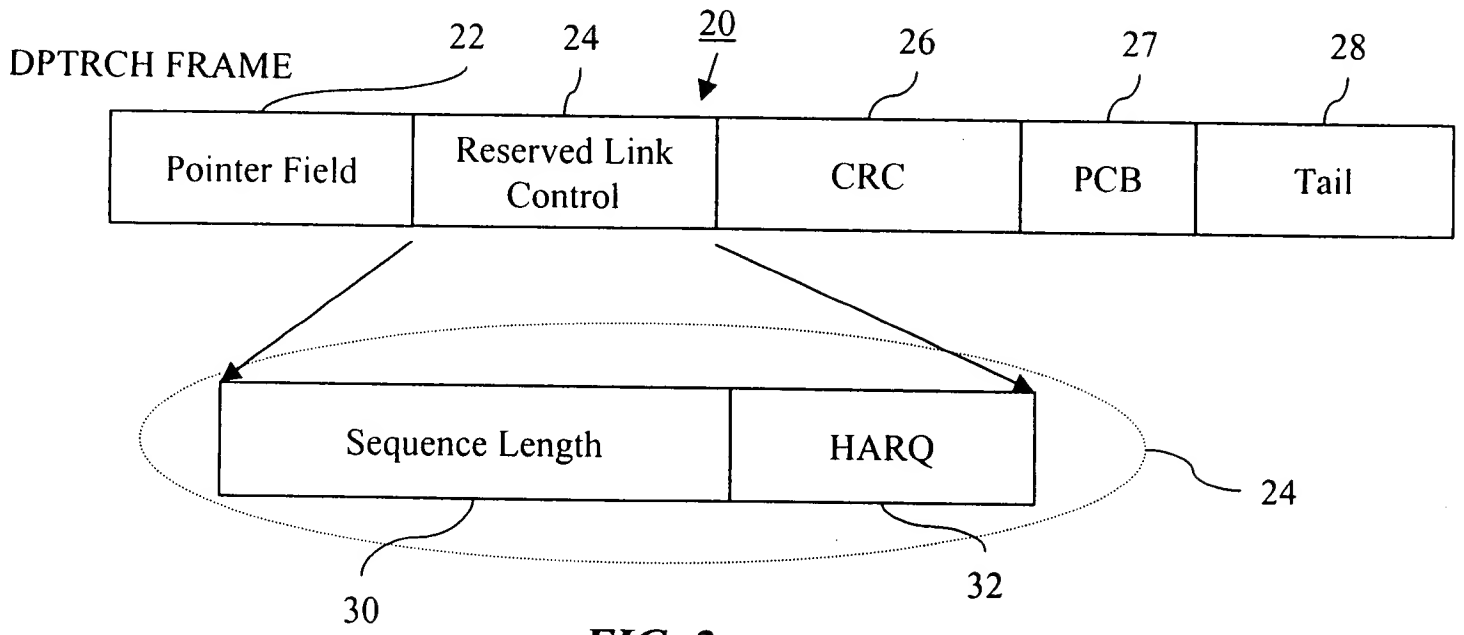

FIG. 2

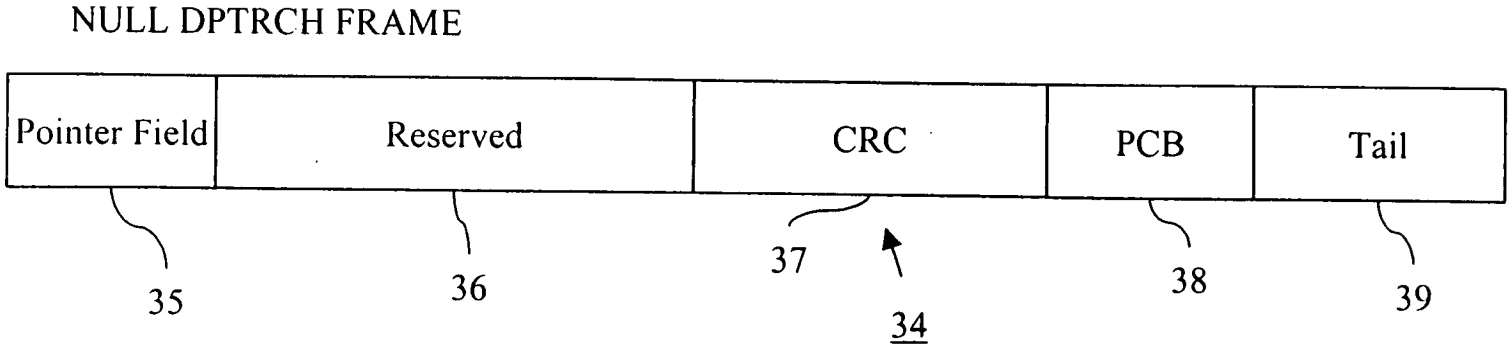

FIG. 3

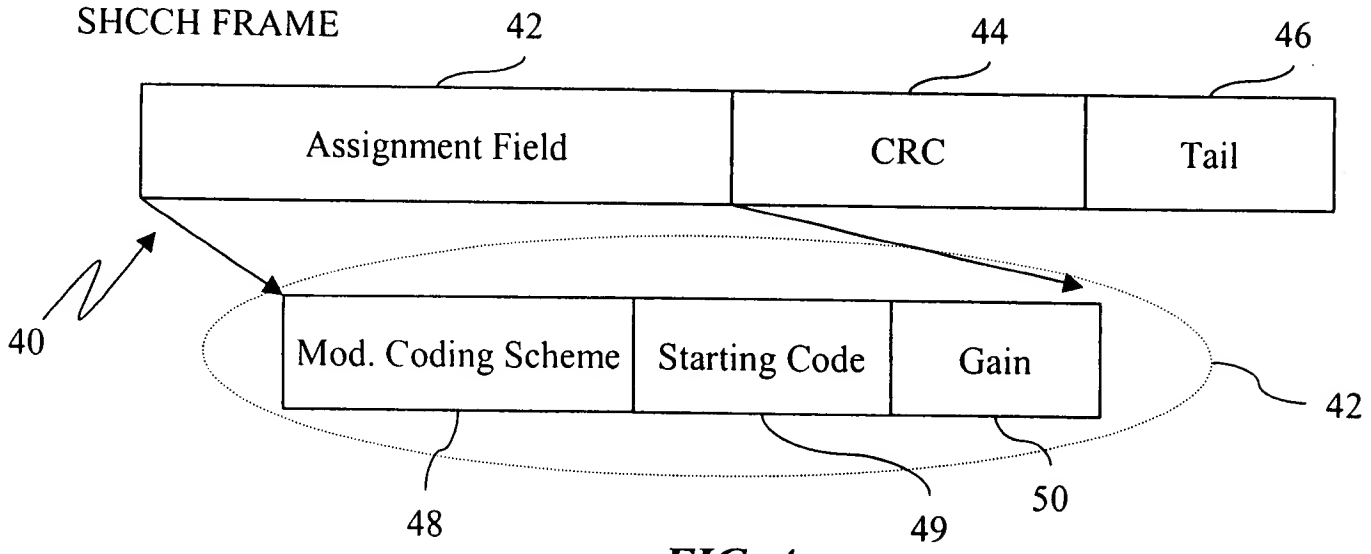
SHCCH FRAME
42
44
46
Assignment Field
CRC
Tail
40
Mod. Coding Scheme
Starting Code
Gain
42
48
49
50

FIG. 4

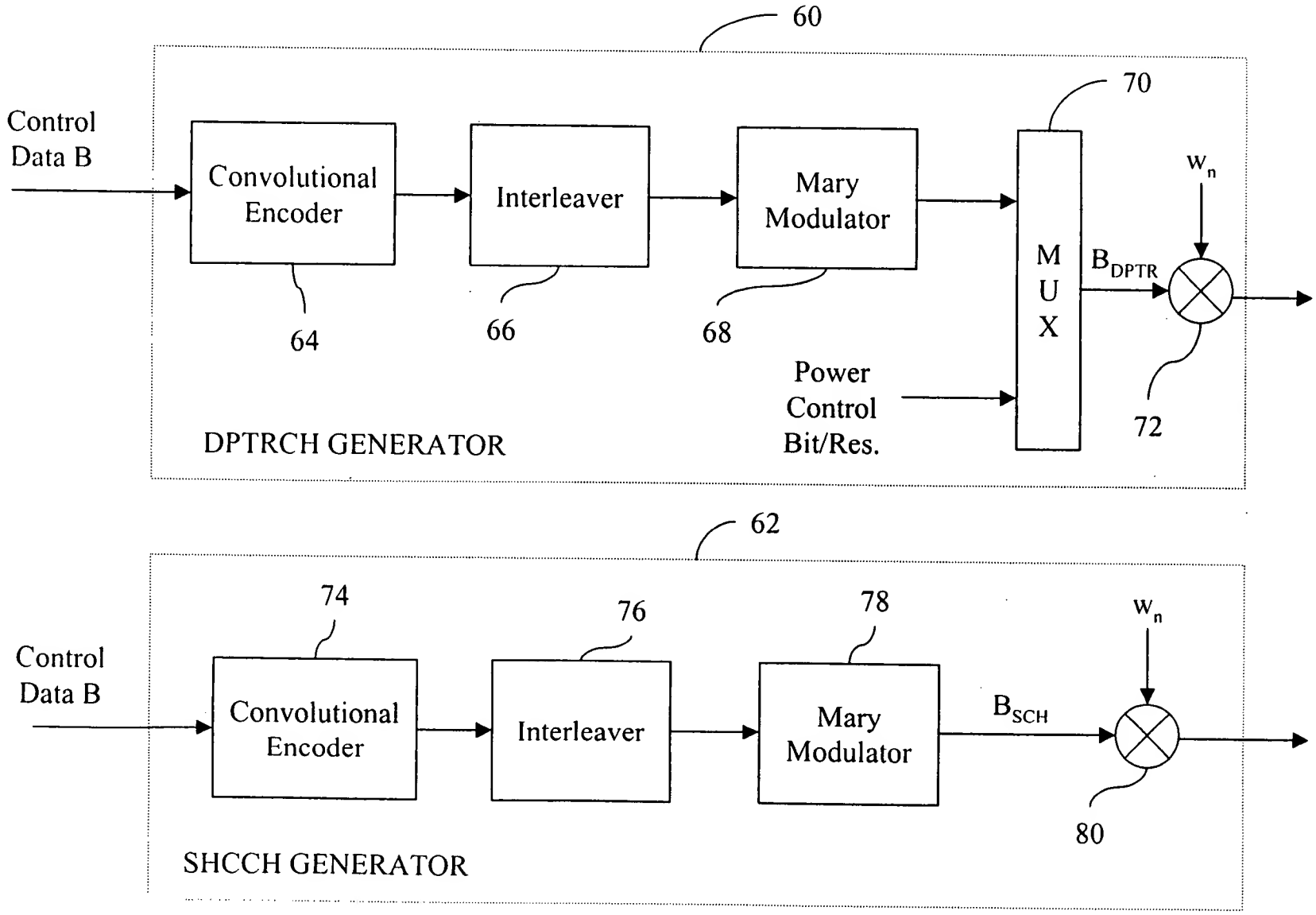
60
Control Data B
Convolutional Encoder
Interleaver
Mary Modulator
70
M U X
B_{DPTR}
w_n
64
66
68
72
Power Control Bit/Res.
DPTRCH GENERATOR
62
74
76
78
Control Data B
Convolutional Encoder
Interleaver
Mary Modulator
B_{SCH}
w_n
80
SHCCH GENERATOR

FIG. 5